TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention [METHOD FOR FABRICATING A MOSFET AND REDUCING LINE WIDTH OF GATE STRUCTURE]

Application Number :

Date:

First Named Applicant: Mr. ERH-KUN LAI

Confirmation Number:

Attorney Docket Number: 11760-US-PA

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Submitted By:	Elec. Sign.	Sign. Capacity
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Documents being submitted:	Files
-	
us-assignment	11760-usassn.xml
	us-assignment.xsl
	us-assignment.dtd
	11760ASSIGNMENT1.tif
	11760assignment2.tif
us-request	11760-usrequ.xml
	us-request.dtd
	us-request.xsl
us-fee-sheet	11760-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
us-declaration	11760declaration02.tif
application-body	11760-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
	isocyr1.ent
	isocyr2.ent
	isodia.ent
	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent
	mmlalias.ent
	soextblx.dtd
	11760us0001.tif
	11760us0002.tif
	11760us0003.tif
us-declaration	11760DECLARATION01.tif
Comments	

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